



RAN - 1803000201030092

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F. Y. B. Sc. Electronics (Sem. - I) Examination

March - 2023

Electronics : Paper - II

Digital Electronics Circuits

[Total Marks: 50

સૂચના : / Instructions

(1)

નીચે દર્શાવેલ નિશાનીવાળી વિગતો ઉત્તરવહી પર અવશ્ય લખવી.
Fill up strictly the details of signs on your answer book

Name of the Examination:

F. Y. B. Sc. Electronics (Sem. - I)

Name of the Subject :

Electronics : Paper - II Digital Electronics Circuits

Subject Code No.: **1803000201030092**

Seat No.:

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Student's Signature

- (2) Figure on the right indicates full marks
- (3) All symbols and abbreviations have their usual meaning.
- (4) Non-programmable calculators are allowed.
- (5) Assume data if necessary.

***O.M.R. Sheet ભરવા અંગેની અગત્યની સૂચનાઓ આપેલ
O.M.R. Sheetની પાછળ છાપેલ છે.***

***Important instructions to fillup O.M.R. Sheet
are given on back side of the provided O.M.R. Sheet.***

- Q. 7.** Which of the following combinations cannot be combined into K-map groups?
- A. Corners in the same column B. Corners in the same row
C. Overlapping combinations D. Diagonal corners
- Q. 8.** The active switching element used in all TTL circuits is the _____.
- A. BJT B. FET
C. UJT D. MOSFET
- Q. 9.** Which of the following is an invalid BCD code?
- A. 0011 B. 1100
C. 1000 D. 0001
- Q. 10.** The binary addition $1 + 1 + 1$ gives
- A. 111 B. 11
C. 110 D. 3
- Q. 11.** A multiplexer has _____.
- A. One input and several outputs
B. One input and one output
C. Several inputs and several outputs
D. Several inputs and one output
- Q. 12.** An AND gate
- A. Implements logic addition
B. Is equivalent to a series switching circuit
C. Is an any-or-all gate
D. Is equivalent to a parallel switching circuit

SPACE FOR ROUGH WORK